

### **REMARKS**

Claims 1, 2, 4-6, 8 and 9 are pending in the application.  
Claims 1, 2, 4-6, 8 and 9 are rejected.

Claims 1 and 5 were rejected under 35 USC 102(e) as being anticipated by Nguyen. Claims 2, 4, 6, 8, and 9 were rejected under 103(a) as being unpatentable over Nguyen in view of Boynton.

Claim 1 includes ... a multiplexer coupled between the memory and the plurality of interconnection pads wherein the universal interface device concurrently interfaces with the plurality of different peripheral devices using time multiplexing of the plurality of interconnection pads. The references of record do not show, teach, or suggest the above recited limitation of claim 1. The Nguyen reference does not disclose a multiplexer for time multiplexing coupled between the memory and the plurality of interconnection pads wherein the universal interface device concurrently interfaces with the plurality of different peripheral devices. The multiplexer disclosed in Claim 15 of Nguyen is not referred to as a time multiplexer coupled between the memory and the interconnection pads. Nothing in Nguyen suggests that the multiplexer is for time multiplexing. Claims 2, 4-6, 8, and 9 depend from claim 1. Therefore claims 1, 2, 4-6, 8 and 9 are believed to be allowable over the references of record.

It is believed that the above remarks are fully responsive to the Official Action. Reconsideration and allowance are therefore respectfully requested.

Respectfully submitted,

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